

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	206	(convert\$5 create\$5 generat\$5) SAME (circuit NEAR5 design) SAME (circuit NEAR5 layout) SAME file	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/11/22 17:49
L4	47	L1 AND HDL	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/11/22 17:38
L6	8	circuit SAME layout SAME file SAME ALLEGRO	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/11/22 17:25
L7	6	(convert\$5 create\$5 generat\$5) SAME circuit SAME layout SAME file SAME ALLEGRO	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/11/22 17:27
L8	25	automatic SAME (convert\$5 create\$5 generat\$5) SAME (circuit NEAR5 design) SAME (circuit NEAR5 layout) SAME file	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/11/22 17:50

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Web

Results 1 - 10 of about 1,000,000 for **converting circuit design file to circuit layout file**. (0.34 seconds)

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[PDF] [EE 41458/61458 Microwave Circuit Design and Measurements Lab ...](#)

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Set up the **circuit** for optimization of the lengths of the stubs and widths of any ... **layout**.

Save this new **design** USING A DIFFERENT FILE NAME so that you ...

www.nd.edu/~hscdlab/labs/lab8.pdf - [Similar pages](#)

[PIC Programmer](#)

Then this PIC programmer downloads the hex **file** into the PIC where their program runs: ...

Yes, this is a good **design** for in-circuit programming. ...

www.bobblick.com/techref/projects/picprog/picprog.html - 29k - [Cached](#) - [Similar pages](#)

[EverythingPCB : Software: PCB Layout / Design / Schematic Capture ...](#)

Multi-sheet schematic entry, **circuit** simulation, rule driven PCB **layout**, fast autorouting ...

configurable through ASCII **file** for easy definition of your own ...

www.everythingpcb.com/p14120.htm - 30k - [Cached](#) - [Similar pages](#)

[FTP Index. Main Library Files](#)

Design Cycle Aug06.zip This zip **file** includes a pcb board **layout** and source codes ... This

Zip **file** contains the ASM **file**, parts placement and **circuit** board ...

www.nutsvolts.com/ftpindex.htm - 107k - [Cached](#) - [Similar pages](#)

[PDF] [Lab 1c PCB layout of RAM interface](#)

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Next, **layout** the **circuit** using the. ExpressPCB program (see starter **file** Lab1prep.pcb). ...

Avoid 90-degree turns, **convert** them to two 45 degree turns, ...

www.ece.utexas.edu/~valvano/metrowerks/Lab01c.pdf - [Similar pages](#)

[Circuit Mill How To - Qwiki](#)

Altium DXP: **Circuit** CAD. The **design** cycle typically begins with Altium DXP. ... IsoPro is a software package to **convert** raw **layout** files (Gerber, ...

qwiki.caltech.edu/wiki/Circuit_Mill_How_To - 25k - [Cached](#) - [Similar pages](#)

[Steps on How to Save Layout from Libra to interface to the CircuitCam](#)

How to Save **Layout** as HPGL **file** to interface to the CircuitCam Software. 1. When you

finish all your micro-strip-line **circuit** **design** in a Schematic Window, ...

www.ece.utah.edu/~cfurse/Tutorials/ADS/circuitcamADS.html - 13k -

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[PDF] [Converting ADS layout to HPGL file for LPKF circuit board milling ...](#)

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Converting ADS **layout** to HPGL **file** for LPKF **circuit** board milling machine ... When you finish all your micro-strip-line **circuit** **design** in a Schematic Window ...

homepages.north.londonmet.ac.uk/~virdeeb/Content/ADS/ADS_to_HPGL_for_LPKF.pdf -

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[Agilent | E9008 High Speed Analog Designer](#)

Frequency-domain **circuit** characterization models wide bandwidth performance for ...

Seminar **Files** and Papers: High Speed Electro-Optical **Design** Seminar, ...

eesof.tm.agilent.com/products/e9008a-new.html - 38k - [Cached](#) - [Similar pages](#)

[PCB-pcb printed circuit boards](#)

PCB **Design** **Layout** PCB **Design** Services PCB **File** Conversions PCB Global Partners ...

P.A.D.S - manufacture of printed **circuit** board (PCB) **design** technologies. ...

www.pcbpro.com/pcb-links.php - 45k - [Cached](#) - [Similar pages](#)

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